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Banpil announces new Agilent ADS compatible Interconnect Design Simulation Library

Banpil Library enables high-speed low-power designs and available for free download

Santa Clara, California – August 30, 2007 - Banpil Photonics, Inc., a leading company in expanding the boundaries of optics and electronics through innovations, today announced the availability of its high-speed interconnects design simulation library that is compatible with Agilent's Advanced Design System (ADS) software. The Banpil Interconnects Simulation Library (BISL) is a first of its kind library that enables system architects to design and test chip-to-chip interconnects capable of more than 20 Gb/s data transmission. The simulated designs utilize Banpil's patented metallic (electrical) interconnects technology. The remarkable channel efficiency of Banpil's metallic interconnects in conventional materials make it possible to increase signal-carrying capacity by more than 6 times over conventional interconnect solutions, while significantly reducing power consumption.

BISL is compatible with ADS, a powerful, widely used electronic design automation (EDA) software system, which offers complete design integration to designers of products such as cellular and portable phones, pagers, wireless networks, and radar and satellite communications systems. ADS is the industry leader in high-frequency design and supports system and RF design engineers developing all types of RF designs, from simple to the most complex, from RF/microwave modules to integrated MMICs for communications and aerospace/defense applications.

BISL adds to the ADS rich set of simulation technologies ranging from frequency- and time-domain circuit simulation to electromagnetic field simulation, by enabling designers to fully characterize and optimize 10 Gb/s and above designs for next generation applications in high-speed systems. System architects are now able to incorporate Banpil high-speed interconnects and other components into the familiar ADS single, integrated design environment, which provides system and circuit simulators, along with schematic capture, layout, and verification capability.

"The Banpil Interconnect Simulation Library for ADS is an important step for us because we are now able to provide system designers with a first-hand opportunity to see the significant performance enhancements that our high-speed metallic interconnects are capable of providing," said Dr. Achyut Dutta, Banpil's CEO. "We have made announcements in recent weeks and also last year regarding our significant breakthroughs in high-speed interconnects requiring significantly less power to drive signals. This library makes >10 Gb/s signals in over a 1.5-meter long rigid FR4 printed circuit board (PCB) and >20 Gb/s in over 2 meters of flexible printed circuits (FPC), a reality that architects can design and simulate into their specific practical applications today."

BISL is available for free download from Banpil's website www.banpil.com/bisl.htm. BISL requires the user to have ADS already installed on their computer and it is installed as an add-on simulation library. Installation instructions are provided. After designing and running simulations of Banpil high-speed interconnects, users need to contact Banpil regarding production implementation and licensing options.

About Banpil Photonics, Inc.
Banpil Photonics develops and licenses fundamental technology expanding the boundaries of optics and electronics. The company has developed an extensive IP portfolio of high-speed interconnects, multispectral image sensors, and high-efficiency photovoltaic technologies. Banpil innovations enable the development and manufacture of new generations of low-cost, high-speed electrical interconnects for chip-to-chip, chip-to-board, board-to-board, and rack-to-rack applications; multispectral image sensors for automotive and medical imaging, remote-sensing, and communication applications; and photovoltaic technology for solar cell applications. For more information, visit www.banpil.com.

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